

In the Claims:

1. (Previously Presented) A system, comprising:

a synchronizer signal generator device, to be connected to an electronic system, the synchronizer signal generator device to emit a synchronizer signal of a particular frequency, the synchronizer signal to be transferred to at least one receiving device of the electronic system;

at least one additional device, for which an impedance is chosen such that a resonance-oscillatory circuit is created for the synchronizer signal generator device having a resonance frequency which essentially coincides with the frequency of the synchronizer signal; and

a clock generator device to generate a clock signal having a frequency, wherein the clock generator device is controlled by the synchronizer signal, and the frequency of the synchronizer signal is greater than the frequency of the clock signal.

2. (Previously Presented) The system according to Claim 1, wherein the synchronizer signal transferred to the at least one receiving device is essentially sinusoid.
3. (Previously Presented) The system according to Claim 1, further comprising a driver device to generate the synchronizer signal.
4. (Previously Presented) The system according to Claim 3, wherein the driver device generates an essentially rectangular signal.

5. (Previously Presented) The system according to Claim 4, wherein the rectangular signal generated by the driver device is to be filtered such that the signal emitted by the synchronizer signal generator device is essentially sinusoid.
6. (Previously Presented) The system according to Claim 1, further comprising at least one impedance device, which has an inductive component.
7. (Previously Presented) The system according to claim 6, wherein the at least one impedance device has a capacitive component.
8. (Previously Presented) The system according to Claim 7, wherein an inductivity and/or capacitance adjustment of the inductive and/or capacitive component is set during manufacture.
9. (Previously Presented) A system according to Claim 8, wherein the inductivity and/or the capacitance of the inductive and/or capacitive component is variably adjustable after manufacture.
10. (Previously Presented) The system according to Claim 9, wherein the capacitive component is a capacitive diode.
11. (Previously Presented) The system according to Claim 1, wherein the at least one receiving device to which the synchronizer signal is to be transferred, is a semi-conductor component.

12. (Previously Presented) The system according to Claim 1, wherein the synchronizer signal is to be used for chronological co-ordination of relaying and/or processing and/or transfer of data.
13. (Previously Presented) The system according to Claim 1, wherein the synchronizer signal generator device generates a further signal under control of the synchronizer signal, which is to be used for chronological co-ordination of relaying and/or processing and/or transfer of data.
14. (Previously Presented) The system according to Claim 13, wherein the further signal has a lower frequency than the synchronizer signal.
15. (Previously Presented) The system according to Claim 14, wherein a PLL or DLL circuit is used to generate the further signal.
16. (Previously Presented) A process for generating a synchronizer, comprising:
emitting a synchronizer signal from a synchronizer signal generator device to at least one receiving device of an electronic system;
providing at least one additional device, for which an impedance has been selected such that, a resonance-oscillatory circuit is created for the synchronizer signal generator device, the resonance-oscillatory circuit having a resonance frequency which essentially coincides with a frequency of the synchronizer signal; and
generating a clock signal at a clock generator device, wherein
the clock generator device is controlled by the synchronizer signal, and
the frequency of the synchronizer signal is greater than a frequency of the clock signal.